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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/556,351	04/11/2006	Tuyosi Ataka	P28766	6740
7055 7590 05/30/2008 GREENBLUM & BERNSTEIN, P.L.C. 1950 ROLAND CLARKE PLACE RESTON, VA 20191			EXAMINER WONG, ALAN	
			ART UNIT 2817	PAPER NUMBER
			NOTIFICATION DATE 05/30/2008	DELIVERY MODE ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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Office Action Summary	Application No. 10/556,351	Applicant(s) ATAKA ET AL.	
	Examiner ALAN WONG	Art Unit 2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-17 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 November 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. ____. |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>2/17/06</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Objections

1. Claim 5, 10-12 are objected to because of the following informalities:
2. With respect to claim 5, line 4; claim 10, line 4; claim 11, line 4; and claim 12, line 4: "a second" appears that it should be --a **first**--; and claim 5, line 6; claim 10, line 6; claim 11, line 6; and claim 12, line 6: "a first" appears that it should be --a **second**--. The reason is "first" should appear before the "second" and it is expected that the unwanted portion is grounded first before acquiring a signal. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claim 1, 2, 4, 5, 7, 10, 13, 14 are rejected under 35 U.S.C. 102(b) as being anticipated by JP 02-130118U cited by applicant.
5. With respect to claim 1 and 13, JP 02-130118U discloses (solely relying on the drawings) an input signal processing device (Fig. 1), comprising: an input signal terminal (1) that receives an input signal; a connection inductance element (18) connected to said input signal terminal (1) at one end thereof; a connection capacitance element (15) connected to said input signal terminal (1) at one end thereof; a first grounding switching means (14) that selectively grounds an other end of said

connection inductance element (18); and a second grounding switching means (13) that selectively grounds an other end of said connection capacitance element (15).

6. With respect to claim 2 and 14, JP 02-130118U discloses a grounding capacitance element (20) that is connected to the other end of said connection inductance element (18), and is grounded; and a grounding inductance element (17) that is connected to the other end of said connection capacitance element (15), and is grounded.

7. With respect to claim 4 and 7, JP 02-130118U discloses when switch 14 is closed, the low pass portion 12 would be grounded and made the whole circuit effectively an high pass filter with only the high pass portion 11 connecting between the input 1 and element 6. This meets the claim on turning on the grounding switch for inductance element to make the circuit becomes high pass filter so that high frequency signals can be obtained.

8. With respect to claim 5 and 10, JP 02-130118U discloses when switch 13 is closed, the high pass portion 11 would be grounded and made the whole circuit effectively an low pass filter with only the low pass portion 11 connecting between the input 1 and element 6. This meets the claim on turning on the grounding switch for capacitance element to make the circuit becomes low pass filter so that low frequency signals can be obtained.

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

10. Claim 3, 6, 8, 9, 11, 12, 15-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP 02-130118U.

11. With respect to claim 3, 6, 15, JP 02-130118U does not disclose explicitly the switching means comprises one of a semiconductor switch and a MEMS switch.

However, at the time of the invention, it would have been obvious to one of ordinary skill in the art to use one of semiconductor switch and MEMS switch in place of the generic switch disclosed by JP 02-130118U. The suggestion to do so is that it would have been obvious to one of ordinary skill in the art to substitute for a generic switch (Fig. 1 item 14, 13) with a known art recognized equivalent switch (such as semiconductor/MEMS switch; e.g. JP 2001-136002 cited by applicant) as a result of utilizing the device.

12. With respect to claim 8, 9, and 16, JP 02-130118U discloses when switch 14 is closed, the low pass portion 12 would be grounded and made the whole circuit effectively an high pass filter with only the high pass portion 11 connecting between the input 1 and element 6. This meets the claim on turning on the grounding switch for inductance element to make the circuit becomes high pass filter so that high frequency signals can be obtained.

13. With respect to claim 11, 12, and 17, JP 02-130118U discloses when switch 13 is closed, the high pass portion 11 would be grounded and made the whole circuit effectively an low pass filter with only the low pass portion 11 connecting between the input 1 and element 6. This meets the claim on turning on the grounding switch for

capacitance element to make the circuit becomes low pass filter so that low frequency signals can be obtained.

Conclusion

14. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Watanabe et al. (US 7,218,185) and Kirby (US 4,095,198) disclose using inductor and capacitor to acquire different frequency signal.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ALAN WONG whose telephone number is (571)272-3238. The examiner can normally be reached on Mon-Thurs 8:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bob Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

**/BENNY LEE/
PRIMARY EXAMINER
ART UNIT 2817**

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